

Notice of References CitedApplication/Control No.
09/651,425Applicant(s)/Patent Under
Reexamination
SONGER ET AL.Examiner
Tuan A VuArt Unit
2124

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,009,256	12-1999	Tseng et al.	703/13
	B	US-5,535,406	07-1996	Kolchinsky, Alexander	712/10
	C	US-6,457,116	09-2002	Mirsky et al.	712/16
	D	US-5,752,035	05-1998	Trimberger, Stephen M.	717/153
	E	US-6,263,302	07-2001	Hellestrand et al.	703/17
	F	US-6,226,776	05-2001	Panchul et al.	716/3
	G	US-6,339,836	01-2002	Eisenhofer et al.	716/5
	H	US-2002/0059456	05-2002	Ha et al.	709/246
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Efficient and Flexible Cosimulation Environment for DSP Applications", Wongyong Sung, Soonhoi Ha; Dec. 1998, IEICE Trans. Fundamentals, Vol. E81-A, No. 12, pp. 2605--2611
	V	"An Integrated Hardware-Software Cosimulation Environment for Heterogeneous Systems Prototyping", Yongjoo Kim et al., June 1996, In Proc. of Seventh IEEE International Workshop on Rapid System Prototyping, pages 66-71
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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